



ISSUE CLASSIFICATION	
Class	Subclass



U.S. UTILITY Patent Application

19869

 Q.I.P.E. <u>Max G. O. U. S.</u> Q.A.	PATENT DATE
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APPLICATION NO. 09/702870	CONT/PRIOR F	CLASS 257	SUBCLASS 6610	ART UNIT 2811 2827	EXAMINER J. D. R. M.
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ANTS Keiichi Den

Semiconductor device of chip-on-chip structure

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PTO-2040
12/99

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="text-align: center;">(Assistant Examiner) _____ (Date) _____</div>			NOTICE OF ALLOWANCE MAILED	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<div style="text-align: center;">(Primary Examiner) _____ (Date) _____</div> <div style="text-align: center;">(Legal Instruments Examiner) _____ (Date) _____</div>			Amount Due	Date Paid
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